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To: Office of Initial Patent
 Examination

From: Mikio Ishimaru

Fax: (703) 746-9195

Pages: 8

Phone:

Date: September 22, 2003

Re: U.S. Patent Application Serial CC:
 No. 10/608,883

☒ Response/Amendment to Office Action

☐ Information

☐ Other

IMPORTANT

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Dear Examiner :

Attached are a Certificate of Transmission, Transmittal and Request for Correction of Filing Receipt, Filing Receipt dated 9/19/03, and Application Data Sheet filed 6/26/03, for U.S. Patent Application Serial No. 10/608,883 (attorney docket no. F0361.C1.D1).

Respectfully submitted,



Mikio Ishimaru
 Reg. No. 27,449

PTO/SB/97 (05-03)

Approved for use through 4/30/2003. OMB 0851-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office

on September 22, 2003
Date



Signature

Justin Zahrt

Typed or printed name of person signing Certificate

Note: Each paper must have its own certificate of transmission, or this certificate must identify each submitted paper.

With reference to serial number 10/608,883, the following are being submitted:

Fax Cover Sheet
Certificate of Transmission
Transmittal and Request for Correction of Filing Receipt
Filing Receipt dated 9/19/03
Application Data Sheet filed 6/26/03

This collection of information is required by 37 CFR 1.8. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.8 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Steven C. Avanzino et al.	Confirmation No.: 7845
Application No.:	10/608,883	Group Art Unit: 2811
Filed:	6/26/2003	Examiner: Unknown
Title:	AN INTEGRATED CIRCUIT WITH DIELECTRIC DIFFUSION BARRIER LAYER FORMED BETWEEN INTERCONNECTS AND INTERLAYER DIELECTRIC LAYERS	

TRANSMITTAL AND REQUEST FOR CORRECTION OF FILING RECEIPT

Office of Initial Patent Examination's
Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application. Enclosed is a copy of the Filing Receipt with the changes noted thereon. There is an error with respect to the following, which is incorrectly entered.

Error inCorrect data

Title: AN INTEGRATED CIRCUIT WITH DIELECTRIC DIFFUSION
BARRIER LAYER FORMED BETWEEN INTERCONNECTS
AND INTERLAYER DIELECTRIC LAYERS

Transmitted herewith are the documents for the above-identified application:

- ☒ Filing Receipt with Correct title hand written
- ☒ Copy of Application Data Sheet filed with Application on 6/26/03
- ☒ No additional fee is required

At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 01-0365 pursuant to 37 CFR 1.25.

Respectfully submitted,



Mikio Ishimaru
Reg. No. 27,449
September 22, 2003

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/608,883	06/26/2003	2811	750	F0361.C1.D1	2	8	2

CONFIRMATION NO. 7845

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FILING RECEIPT

10/608,883 06/26/2003 2811 750 F0361.C1.D1 2 8 2
 OC000000010882741

Date Mailed: 09/17/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Steven C. Avanzino, Cupertino, CA;
 Pin-Chin Connie Wang, Menlo Park, CA;
 Minh Van Ngo, Fremont, CA;

RECEIVED

SEP 19 2003

MIKIO ISHIMARU

Domestic Priority data as claimed by applicant

This application is a DIV of 10/226,520 08/22/2002
 which is a CON of 09/874,175 06/04/2001 PAT 6,469,385

Foreign Applications

If Required, Foreign Filing License Granted: 09/15/2003

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

An Integrated circuit with dielectric diffusion barrier layer formed between interconnects and interlayer

dielectric layers

Preliminary Class

257

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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